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11/14/03
Muller

Attorney Docket SEL 192

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of Yamazaki et al.)

Serial No.: 09/610,217 ✓)

Filed: July 5, 2000 ✓)

For: Semiconductor Device And)
Fabrication Method Thereof)

Art Unit: 2812)

Examiner: R. Booth)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231 on December 20, 2002 (Date of Deposit)

Rachelle Hammerquist
Name of applicant, assignee, or Registered Rep.
Rachelle Hammerquist 12-20-02
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Commissioner for Patents
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December 20, 2002

AMENDMENT A

Sir:

In response to the Office Action of August 22, 2002, a one month extension of time being separately submitted, please amend the above-identified application as follows:

IN THE CLAIMS:

Please amend the claims as follows:

A¹ 29 (Amended). A method of fabricating a semiconductor device having, on the same substrate, a pixel TFT disposed in a pixel unit and a driving circuit including a p channel type TFT and an n channel type TFT and disposed round said pixel unit, said method comprising:

forming an underlying film over said substrate;

forming a plurality of semiconductor islands over said underlying film;